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**SUPPLEMENTAL INFORMATION  
DISCLOSURE STATEMENT BY APPLICANTS**

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Sheet 1 of 2

**Complete if Known**

Application Number	09/699,019
Filing Date	October 27, 2000
First Named Inventor	A. Rofougaran
Group Art Unit	2682
Examiner Name	M. Milord
Attorney Docket Number	15258US05

**U.S. PATENT DOCUMENTS**

Examiner Initial*	Cite No. <sup>1</sup>	Document Number Number-Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		4,710,970	12.1987	Wu et al.	
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DATE CONSIDERED

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Sheet 2 Of 2

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Application Number	09/889,019
Filing Date	October 27, 2000
First Named Inventor	A. Rofougaran
Group Art Unit	2682
Examiner Name	M. Milord
Attorney Docket Number	15258US05

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. 1	Foreign Patent Document Country Code* Number * Kind Code * (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
MM		EP 0 803 997 A2	10.1997	Kuiri, Tapio		

**OTHER ART - NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
MM		A. Rofougaran, "A Single-Chip Spread-Spectrum Wireless Transceiver in CMOS," Final Report, Integrated Circuits & Systems Laboratory Electrical Engineering Department, University of California, Los Angeles, California, 339 pages (1998).
MM		J. Lee et al., "Design of Spiral Inductors on Silicon Substrates with a Fast Simulator", 4 pages, Electrical Engineering Department, University of California, Los Angeles, California, presented at the European Solid-State Circuits Conference, The Hague, The Netherlands (September 1998).
MM		P.Y. Chan, "A Highly Linear 1-GHz CMOS Downconversion Mixer," Integrated Circuits & Systems Laboratory, University of California, Los Angeles, California, presented at the European Solid State Circuits Conference, Seville, Spain, 4 pages (September 22-24, 1993).

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